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Chapter 1: Added "Clock Gating for Power Savings" on page 22 . Revised

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Figure 1-2, page 26 . Revised Figure 1-16, page 33. Chapter 2: Revised DCM reset and locking process in "Reset Input - RST," page 49 . Updated DO[2] description in Table 2-4, page 52.

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Chapter 6, 3.3V I/O Design Guidelines. 5. For more flexibility in specific designs, a maximum of 100 user I/Os can be stressed beyond the normal specification for no more than 20% of a data period .

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