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Material and process limits in silicon VLSI technology ...

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S G D S G D N P N P N P WELL P+

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Silicon-based CMOS VLSI nanotechnology at the current state-of-the-art and beyond will be used as the framework to gain an understanding of process physics, process interactions and process integration. Process simulation using commercial TCAD tools will be used extensively to illustrate and explore the technology.

EE/MSE 528 Physics and Modeling of VLSI Fabrication

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