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XLAT || PART 1 ||  
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part 1* Instruction set  
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**Data Transfer Set**

**Instructions** *Data*

*Transfer Instruction*

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Instruction Set String

Manipulation

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Arithmetic Instructions

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Arithmetic and Logical

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Microprocessor -

Microprocessor

Addressing modes of

8086 Learn 8086

(x86) Assembly

Programming -

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Lesson 1: For **Set**

absolute beginners!

**BCD and ASCII**

**instructions |VTU|**

**|ECE| |CSE|**

**|Engineering|**

**Assembly Language**

**REG: AX, BX, CX,**

**DX, AH, AL, BL, BH,**

**CH, CL, DH, DL, DI,**

**SI, BP, SP. Instruction**

**set of microprocessor**

**8086 part 2**

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**8 string instructions of**



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80865. Arithmetic

Instructions of 8086

[AAA, AAS, AAM,

AAD, DAA, DAS]

8086 | Block Transfer

Program | String

Instructions | Bharat

Acharya Education

*8086 Rotate, Shift*

*and Processor*

*Control Instructions |*

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**Programming Part 4**

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Instruction Format

and Template String

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*8086 Assembly*

*Language For*

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ADD | SUB | FLAGS

CS305 Module2 8086

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Quick reference: AAA  
AAD AAM AAS ADC  
ADD AND CALL CBW  
CLC CLD CLI CMC  
CMP: CMPSB  
CMPSW CWD DAA  
DAS DEC DIV IDIV  
IMUL IN INC INT  
INTO IRET JA: JAE  
JB JBE JC ...

assembler generates  
several RCL xx, 1  
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Most processors have a “compare” instruction. look at the instruction reference for whatever processor you’re trying to program, and use the instruction you find. compare two operands (cmp)

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`cmp{bwl} reg[8|16|32],`

`r/m[8|16]` `cmp` is

typically executed in

conjunction with

conditional jumps and

the `setcc` instruction.

example. Complete

8086 instruction set

example: `mov al, 5`

`mov bl, 5` `cmp al, bl ;`

`1` instructions

because 8086 has

machine code only for

this instruction `lea`

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Instruction in 8086 ..

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*8086 cmp example in  
instruction -*

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Microprocessor -

8086 Instruction Sets

- The 8086

microprocessor

supports 8 types of  
instructions ? ... STI ?

Used to set the  
interrupt enable flag  
to 1, i.e., enable INTR

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input. CLI ? Used to  
clear the interrupt  
enable flag to 0, i.e.,  
disable INTR input.  
Iteration Control  
Instructions.

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reference: aaa aad  
aam aas adc add and  
call cbw clc cld cli  
cmc cmp: cmpsb  
cmpsw cwd daa das  
dec div hlt idiv imul in  
inc int into iret ja: jae  
jb jbe jc jcxz je jg jge jl  
jle jmp jna jnae jnb:  
jnbe jnc jne jng jnge  
jnl jnle jno jnp jns jnz  
jo jp jpe: jpo js jz lahf  
lds lea les ...

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Complete 8086 Set

*instruction set -*

*KFUPM*

Original 8086/8088

instruction set

Instruction Meaning

Notes Opcode AAA:

ASCII adjust AL after

addition: used with

unpacked binary

coded decimal: 0x37

AAD: ASCII adjust AX

before division:

8086/8088 datasheet

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documents only base  
10 version of the AAD  
instruction (opcode  
0xD5 0x0A), but any  
other base will work.  
Later Intel's  
documentation has  
the generic form too.

Copyright code : aba3  
eba9890ae8d861b2b

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